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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Rajesh Khamankar, *et al.*
Serial No.: 10/702,234
Filed: November 6, 2001
Title: **RELIABLE HIGH VOLTAGE GATE DIELECTRIC
LAYERS USING A DUAL NITRIDATION PROCESS**
Grp./A.U.: 2814
Examiner: Nathan W. Ha

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



Sir:

AMENDMENT UNDER 37 C.F.R. § 1.116

In response to the Examiner's Action mailed June 10, 2005, please amend the above-identified application as follows: